

CONVENTIONS

Abbreviations

Symbol	Units of measure
°C	degree Celsius
Hz	hertz (cycles per second)
Kbyte	kilobyte (1,024 bytes)
kHz	kilohertz
kΩ	kilohm
Mbyte	megabyte (1,048,576 bytes)
MHz	megahertz (1,000 kilohertz)
μF	microfarad
μs	microsecond (1,000 nanoseconds)
mA	milliampere
ms	millisecond (1,000 microseconds)
ns	nanosecond
pV	picovolt

The use of 'tbd' indicates values that are 'to be determined', 'n/a' designates 'not available', and 'n/c' indicates a pin that is a 'no connect'.

Acronyms

Acronym	Definition
AC	alternating current
ALU	arithmetic logic unit
ATE	automatic test equipment
BIOS	basic input/output system
BitBLT	bit boundary block transfer
bpp	bits-per-pixel
CAD	computer-aided design
CLUT	color lookup table
CMOS	complementary metal-oxide semiconductor
CRT	cathode ray tube
DAC	digital-to-analog converter
DC	direct current
DDA	digital differential algorithm
DDC	display data channel
DPMS	display power management signaling
EPROM	electrically programmable read-only memory
EVAFC	extended VESA® advanced feature connector
FIFO	first in/first out
GPIO	general-purpose IO
GSC	graphics system controller
GUI	graphical user interface
HRQ	host read queue
HSYNC/VSYNC	horizontal/vertical synchronization
HWQ	host write queue
IC	integrated circuit
LBI	local bus interface
LSB	least-significant bit
LUT	lookup table
MA	memory arbiter
MC	memory controller
MCC	monochrome-to-color converter
MD	memory data
MMI/O	memory-mapped I/O
MSB	most-significant bit

Acronym	Definition (cont.)
OFU	operand fetch unit
OSU	operand storage unit
PCI	peripheral component interconnect
PFS	programmable frequency synthesizer
PLL	phase-locked loop
PQFP	plastic quad-flat pack
RAC	V-Port™ access channel
RAM	random-access memory
RDRAM	Rambus® dynamic random-access memory
RGB	red, green, and blue
RIF	Rambus® interface
ROPs	raster operations
RSU	result storage unit
R/W	read/write
SC	serial clock
SRAM	static random-access memory
TSR	terminate and stay resident
VDD	virtual device driver
VESA®	Video Electronics Standards Association
VGA	video graphics array
VL	VESA® local
VRAM	video random-access memory
WE	transparency write enable

Numeric Naming

Hexadecimal numbers are represented with all letters in upper case and a lower-case 'h' is appended to them (for example, '14h', '3A7h', and 'C000h' are hexadecimal numbers). Binary numbers are represented with a lower-case 'b' appended. Numbers not indicated by a 'b' or an 'h' are decimal.

REFERENCES

The following documents are referenced in this data book or contain additional relevant information.

- *PCI Local Bus Specification*, PCI SIG, Version 2.0, 1994
- *PowerPC 601 RISC Microprocessor User's Manual*, MPC601UM/AD, Motorola
- *PowerPC Reference Platform Specification*, IBM Corporation, 11400 Burnet Road, Austin, TX 78758
- *How to Create Endian-Neutral Software for Portability*, James R. Gillig, IBM Paper #TR54.837
- *Alpine™ VGA Family-GD543X/4X Technical Reference Manual*, Cirrus Logic Inc., February 1995, Order# 385439-004
- *RDRAM® Reference Manual*, Rambus, Inc., Version 1.0, DL0007-01
- *Programmer's Guide to the EGA and VGA Cards*, Ferraro, Ricard F., Third Edition, ISBN 0-201-62490-7
- *Fundamentals of Interactive Computer Graphics*, Foley, J.D. and Van Dam, A., ISBN 0-201-14468-9